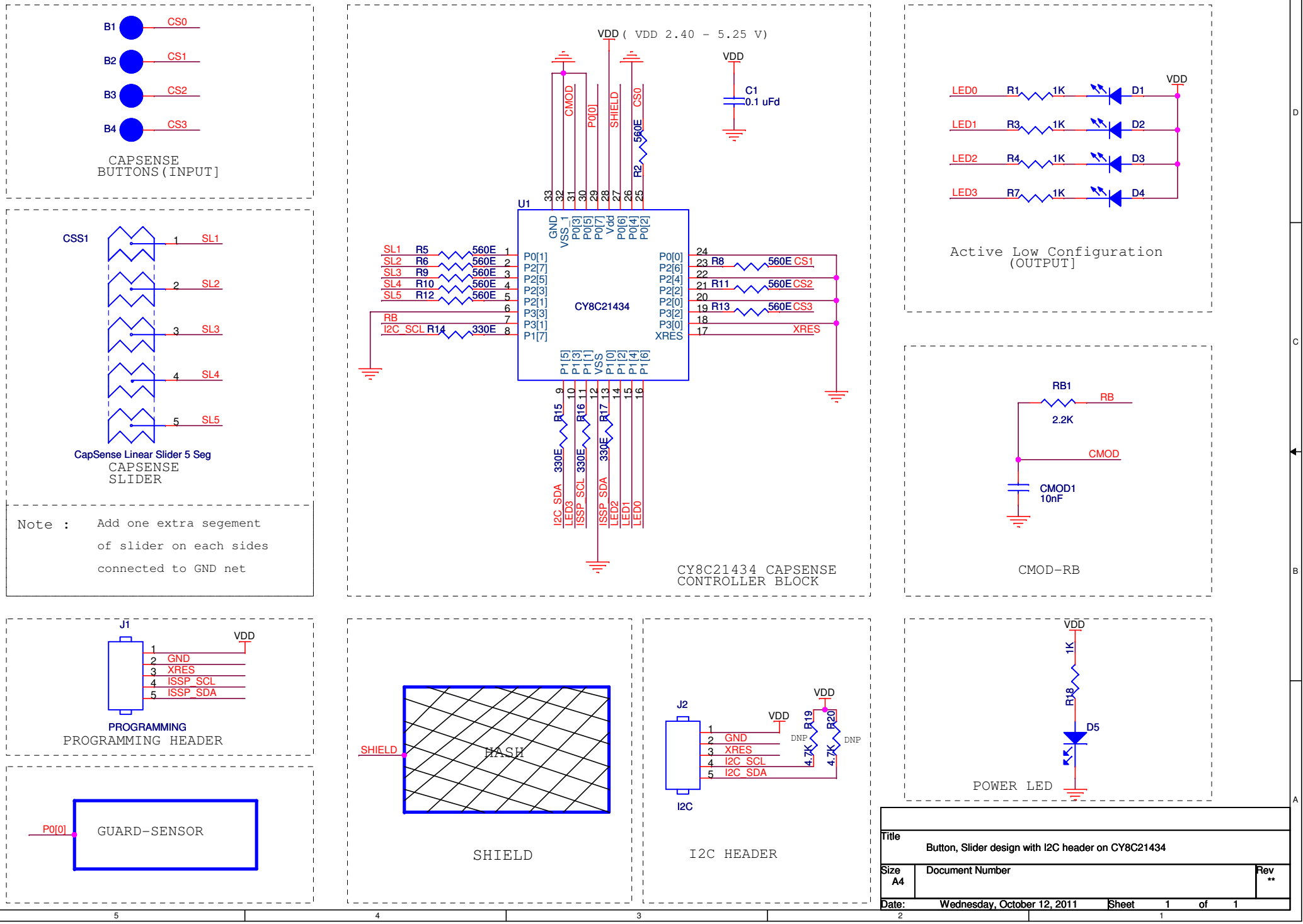
B1
CS0
B2
CS1
B3
CS2
B4
CS3
CAPSENSE
BUTTONS(INPUT]
CSS1
SL1
SL2
SL3
SL4
SL5
CapSense Linear Slider 5 Seg
CAPSENSE
SLIDER
Note : Add one extra segement of slider on each sides connected to GND net
VDD (VDD 2.40 - 5.25 V)
VDD
C1
0.1 uFd
CMOD
P0[0]
SHIELD
CS0
560E
R2
U1
CY8C21434
SL1 R5 560E
SL2 R6 560E
SL3 R9 560E
SL4 R10 560E
SL5 R12 560E
RB
I2C_SCL R14 330E
R8 560E CS1
R11 560E CS2
R13 560E CS3
XRES
R15 330E
R16 330E
R17 330E
I2C_SDA
LED3
ISSP_SCL
ISSP_SDA
LED2
LED1
LED0
CY8C21434 CAPSENSE
CONTROLLER BLOCK
LED0 R1 1K D1
LED1 R3 1K D2
LED2 R4 1K D3
LED3 R7 1K D4
Active Low Configuration
(OUTPUT]
RB1
2.2K
RB
CMOD
CMOD1
10nF
CMOD-RB
J1
PROGRAMMING
PROGRAMMING HEADER
GND
XRES
ISSP_SCL
ISSP_SDA
SHIELD
HASH
SHIELD
J2
I2C
I2C_SCL
I2C_SDA
R19 4.7K DNP
R20 4.7K DNP
I2C HEADER
P0[0]
GUARD-SENSOR
R18 1K
D5
POWER LED
Title
Button, Slider design with I2C header on CY8C21434
Size A4
Document Number
Rev **
Date: Wednesday, October 12, 2011
Sheet 1 of 1